

Flash-IOT

# Weekly Team Meeting 04/02

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02 April 2020 / 3:00 PM / ZOOM 257000043

## ATTENDEES

Dr. Ay, Dr.Li, Xueren Wang, Wei Hu, Zhicai Li, Yanqin Huang, Yuhang Dai

## AGENDA

- Show the case for the box.
- Showing the second version of TR module.
- Show the code for the test.

## NOTES

- Wei go over the second version of TR module.
  - It's too high for the inverting amplifier that will decrease the bandwidth of the signal.
  - Making the subtractor gain of 1, use 330 or 200k for the second stage. Making the first stage to be gain of 1000.
  - To look at the sources of those devices devices to suppress the noise, we can make higher gain at the beginning.
  - We can also suppress the noise by increasing the TIA gain but the problem with that is the bandwidth.

- We put the port in the TIA module 1.0, but it lack of testing port previously. It's hard to measure the output of the first stage. Now, it's become convenient if we add the testing port.
  - We should look at the data sheet, there is noise calculate.
  - For SN74HC14, we have to buy the component with high speed.
  - We also show the Simulation results.
- Yanqin and Yuhang show the case for the box we discussed on Tuesday.
    - There is some improvement in the design of the case for the box
      - We adjusted the position of Arduino. It sticks upright on the top and the edge of the bezel.
      - Two holes were made in the top and above the baffle 2 to introduce light into the PCB.
- Zhicai Li show the code part.
    - He showed the code to test node and showed the time.
    - He showed how long it takes to analyze a set of data.

## ACTION ITEMS

- We will complete the CAD design as soon as possible.
- We will improve the second version of TR module.
- We will complete the debugging of the board code.