

Weekly notes:

Introduction: This is the agenda for the week Ali is the moderator, Orion is the recorder.

Current Events: Laboratory access all team members have access to the lab.

-Block diagram questions, Harley has created a basic design for his block diagram for his code flow and what should go into it.

5:43 Professor Hess joins meeting.

Purpose of block diagrams to layout what our path should go through. It takes inputs from the flywheel and determines what MATLAB should do. Harley's diagram is for his Renishaw encoder.

Project question: MATLAB coder license. We did get the license for the coder.

5:45 Brian shared his screen. MATLAB window showing RTTS tests. The license allows the Simulink Realtime model to be built. Will need to do a test before being able to use the computer.

Speedgoat_test_SN6347_R2020a

Speedgoat_IO323_CI_02217 has the blocks that will be built from. DO NOT change this file. Copy the blocks from the model and use them in the code. Example the analog to digital converter. Going through the blocks. Grab the blocks from here.

Last thing speedgoat Simulink Real-Time test model. (run on target)

The monitor is outputting a visual of waveforms. Loopbacks. Digital signals showing square waves.

That's all Brian has.

One event reminder is the snapshot is due 2 weeks from today.

Summary: Moderator and recorder for the week. All members have lab access. Block diagram introduced and Harley's block is shown. MATLAB coder license has been purchased. The file to get the basic blocks from was shown, this file should not be changed at all. The speedgoat test was displayed and that it can do inputs and outputs. Finally Professor Hess reminded everyone about the snapshot.