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University of Idaho

College of Engineering



BUTTON CYCLER ENHANCEMENTS FOR ADVANCED INPUT SYSTEMS

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VALUE PROPOSITION

Exhaustive testing of human machine interfaces (HMI) associated with high technology equipment is needed to assure that the HMI remain fully functional throughout the anticipated usage life.

The goal of our project is to design a next-generation keyboard testing equipment/software package that can be easily configured to a broad range of keyboard geometries.

The test will reduce human labor required for data acquisition/visualization and provide information about changes in switch health throughout the entire testing procedure.

PROBLEM STATEMENT

Develop enhancements to a button cycler for use in qualification testing of electro-mechanical switches designed into Advanced Input Systems' products.

CURRENT SYSTEM CAPABILITES

- Incremental counters detect and display # of switch closures after reset
- **I** System force can be somewhat controlled through input pressure.
- I Multiple air cylinders (Bimbas) can test multiple keys on a device.

CURRENT SYSTEM DRAWBACKS

- I No way to monitor or adjust force applied by pneumatic Bimbas.
- **I** No way to measure or quantify **switch health**.
- **I** Switch closure count doesn't indicate *when* an error has occurred.

SYSTEM REQUIREMENTS



- Friority scale, structured as tiers (1-4)
 - 1 LabVIEW program, debounce detection, and datalogging
 - Test 1 to 30 switches simultaneously at a rate from 1Hz to 5Hz,
 - Allow operator to assign cycle rate, and cycle count prior to test start.
 - Monitor switch health during cycling:
 - Open/Closed resistance waveform
 - Show present values of data logging during test.
 - Switch bounce that lasts >10ms
 - Allow operator to pause/resume/restart tests and save test setups.
 - Format Excel report at test end.

TENTATIVE SYSTEM REQUIREMENTS



I Priority scale (1-4), structured as tiers (lowest number is highest priority)

- 2 Force input and detection
 - Allow operator to assign cycle force prior to test start.
 - Monitor maximum applied button force during cycling
- 3 Switch characterization
 - Characterize switch's F/D/R behavior at pre-programmed test intervals
 - Output hysteresis curve of results
- 4 Additional hardware redesign
 - Modernize hardware setup for button cycler system with new test apparatus

WHAT IS LABVIEW?



AIS Button Cycler Enhancements Priority 1 System Diagram

SYSTEM HARDWARE

AIS Button Cycler Enhancements Priority 1 System Diagram





Key: Solenoid – S Bimba – B

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SYSTEM SOFTWARE - FUNCTIONALITY

I Program structure (overview) – state machine

Idle

- User can graphically assign test parameters
- Natural state of the program when nothing is happening

Calibrating

Data on the system is obtained that is needed prior to the test

Testing

Information on switch health is recorded and displayed to the user

Logging

A brief system pause is allowed to log data to the device

Paused

So the operator can make adjustments to hardware if needed during the test

- Complete

The test reached its specified number of cycles and is now finished

AIS Button Cycler V0-10

Program State				Completion (%)
1) Select test settings				
Configuration		Test Parameters		
Test Config CSV File Path	_	Test Duration (Cycles)	100	CLEAR ALL
C:\Users\aoverby\Desktop\AIS Capstone\AIS Button Cycler U of I Test Config.csv	a	Cycle Rate (Hz)	5	RECALL
		Failure Criteria (%)	2	RECALL
Analyzation		Data Logging Period (Cycles)	10	
Data Logging Folder Path	_	Closed Resistance Threshold (Ohms)	40	
		Closure Debounce Threshold (ms)	10	
Detailed File Managérie and a D		Open Resistance Threshold (Ohms)	1000	
Data Logging File Name (no extension needed)]	Release Debounce Threshold (ms)	10	
		L	·	

2) Choose cylinders/buttons to test

CLEAR ALL	Cylinder ID	Switch ID	Cylinder Output Channel	Total Failures	Avg. Closed Resistance (Ohms)	Avg. Closure Debounce (ms)	Avg. Open Resistance (Ohms)	Avg. Release Debounce (ms)
	1							
	2							
	3							
	4							
	5							
	6							
	7							
	8							
	9							
	10							

CLOSE VI

J.

SYSTEM SOFTWARE – DEBOUNCE DETECTION



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SYSTEM SOFTWARE – MULTIPLE CYLINDERS

- State control through a Boolean array
- Indexer to keep track of what cylinder will fire
- Timer to tell the cylinder when it needs to extend or retract















TEST ANALYZATION

Capabilities

- Provide a general oversight of the data captured by the LabVIEW Program
- Provide an easy to analyze Data Presentation Format



TEST ANALYZATION-SUMMARY PAGE

A	В	С	D	E	E	G H	I I	J K	L	М	N	0	P	Q	R
			Test Summ	nary											
	Test ID		Test 00	1	mport CSVs	Cylinder ID	1	Cylinder II	D 2	Cvli	inder ID	3	Cyling	ler ID	4
	Total Cvcl	les	1000			Switch ID	Ā	Switch II	D F	SI	witch ID	B	Swit	tch ID	
	Cycle Rate	e	5 Hz	N	Aake Charts	Cvcle #	Fail Type	Cvcle #	Fail Type	C)	/cle#	Fail Type	Cvcl	e #	Fail Type
	Active Cv	linders	4			21	CL DB	21	CL DB		21	CL DB		21	CL DB
					ailure Logs	26	CL DB	26	CL DB		26	CL DB	2	26	CL DB
						27	CL DB	27	CL DB		27	CL DB	2	27	CL DB
						30	CL DB	30	CL DB		30	CL DB	6	30	CL DB
Cylinder ID	Switch ID	Failures	Closed Resistance	Closure Debounce	Open Resistance	38	CL DB	38	CL DB		38	CL DB	3	38	CL DB
1	Α	80	0	80	0	39	CL DB	39	CL DB		39	CL DB	3	39	CL DB
2	E	80	0	80	0	41	CL DB	41	CL DB		41	CL DB	4	11	CL DB
3	В	80	0	80	0	42	CL DB	42	CL DB		42	CL DB	4	12	CL DB
4	;	80	0	80	0	43	CL_DB	43	CL_DB		43	CL DB	4	13	CL DB
						44	CL DB	44	CL DB		44	CL DB	4	14	CL DB
						45	CL DB	45	CL DB		45	CL DB	2	15	CL DB
						47	CL DB	47	CL DB		47	CL DB	4	17	CL DB
						48	CL DB	48	CL DB		48	CL DB	4	18	CL DB
						49	CL DB	49	CL DB		49	CL DB	4	19	CL_DB
						51	CL DB	51	CL DB		51	CL DB	5	51	CL DB
						52	CL_DB	52	CL DB		52	CL DB	5	52	CL_DB
						53	CL DB	53	CL DB		53	CL DB	5	53	CL DB
						54	CL_DB	54	CL_DB		54	CL_DB	5	54	CL_DB
						56	CL_DB	56	CL DB		56	CL DB	5	56	CL DB
						57	CL_DB	57	CL_DB		57	CL_DB	5	57	CL_DB
						58	CL_DB	58	CL_DB		58	CL_DB	5	58	CL_DB
						60	CL_DB	60	CL_DB		60	CL_DB	6	50	CL_DB
						61	CL_DB	61	CL_DB		61	CL_DB	6	51	CL_DB
						63	CL_DB	63	CL_DB		63	CL_DB	6	j3	CL_DB
						70	CL_DB	70	CL_DB		70	CL_DB	5	70	CL_DB
						71	CL_DB	71	CL_DB		71	CL_DB		71	CL_DB
						73	CL_DB	73	CL_DB		73	CL_DB	7	73	CL_DB
						75	CL_DB	75	CL_DB		75	CL_DB		75	CL_DB
						76	CL_DB	76	CL_DB		76	CL_DB	5	76	CL_DB
						77	CL DP	77	CL DP		77	CL DP	5	77	CI DP



BUDGET OVERVIEW

		Year													
Expense Items		Sept		Oct		Nov		Dec	Jan	F	eb	Mar	Apr	14	Total
Equipment/Tools:															
										-		-		\$	1970
Travel:	-		-							-	-		-		
Travel to Cour d'a Alene	\$	100			\$	100							\$ 100	\$	300
	-		-							-	-	0	-	\$	7924
Supplies / Parts / Services:															
McMaster-Carr			\$	205							i.			\$	205
Moscow Building supply			\$	17										\$	17
Amazon			\$	70										\$	70
McMaster-Carr					\$	170					1			\$	170
Amazon	1				\$	104					1			\$	104
National Instruments							\$	387			1			\$	387
											1			\$	19 - 11
											1			\$	1277
											1			\$	(7)
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					-						Grad	uate Stud	ent Support	\$	1,000
												Sho	p Overhead	\$	400
												Universi	ty Overhead	\$	133
													Totals	\$	2.785

DESIGN VALIDATION PLAN

Senior Capstone Design

Project: AIS Button Cycler Enhancements

Primary Author: Chris Crozier Date:

12/2/2019

Team: P.R.E.S.S.

Design Validation Plan & Results (DVP&R)

Requirement	Test	Test Subject	Target Date	Result	Recommendation
Force set and calibrated prior to test start	Use load cell to measure accuracy of Bimba force at variable input settings as well as degree of error over many cycles	Individual Bimbas and full system			
System must function off existing pneumatic supply (100 psi)	Full battery short test	Full system, regulator tanks	10/22/19	Confirmed	
Characterize switch F/D/R at preset intervals	TBD				
Monitor switch resistance	Produce waveform of switch resistance over time	LabView VI	10/23/2019	Confirmed	
Flag switch bounces > 10 ms	Manual test	LabView VI	12/4/2019	Partially complete	

SCHEDULE

	Las	t Updated:												
		Year	2019								1			Ĩ
 1021/02/00 1020/02/00 		Month	Dec		Ja	in			Fe	eb				Ĩ
Task Description	Duration	Assigned	12/30	1/6	1/13	1/20	1/27	2/3	2/10	2/17	2/24	3/2	3/9	I
Team Contract		ALL									1	1	1	t
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SYSTEM BENCHMARKS												J		İ
Actuate 1 Bimba with Solenoid														Î
Install DAQ unit	1				S 1	2 77				1	1	10 I	1 2	İ
Build LabView VI		8	8 3		0.00	e - 6		- 3		8 8	1	£)	£ - 9	Î
Read Open/Closed Resistance (Ohms) of Switch												1		t
Establish Initial Logging Scheme in LabView												11 1	· ·	İ
Fine Tune Calibration Ovele	S	6	8 8				-			3 0	1	6 3	E 1	t
Expand System to 4 Bimbas											1			t
Update Logging Scheme					1	-		-				1	1	t
Evaluate Data Stream			5		1			-		24 24		0		t
Develop and Integrate Eyral Deporting		-		_		- 4	-	-		-	1	-	-	t
Evened Sustain to 20 Bimbac	-		-		1 H		-	-				-		ł
expand system to by bimods			-			-	-	-				-	-	ł
CUDDATE ADD CONTINUE DATA LOPPING					-									-

NEXT STEPS

- 1. Multiple Bimba system
- 2. Complete Priority 1 project requirements
- **3.** Begin Priority 2 project requirements (tentative)
- 4. Document program and provide usage instructions to AIS



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QUESTIONS?