

Meeting Minutes

Started 5:40 pm

Introduction: Harley is the moderator for this week and Ali is the recorder for this week.

Current Events: Computer Lab Schedule has been done.

Semester Plan:

Minimum Expectations:

- Setup the speedgoat blocks, either the ADC/BiSS blocks, and CIS documentation for the above
- Confirm with group members
- Confirm with Brian

Maximum Expectations:

- Basic numerical displays are active with more complex ones in the works

Testing blocks are done with either the basic displays tested or ready to be tested

CIS documentation on all the above

Brian asked what is the plan for the fall Semester? Harley answer him that it is going to be validation and finalization.

Brian asked if the Computer Lab Schedule is editable?

Harley answered him that it can be editable.

Dr. Hess asked what Computer Lab Schedule is.

Brian answered him that it is an access schedule for the development equipment.

Snapshot feedback: Dr Hess, Brian, Orion, and Harley all agreed that we as the group member we need to learn more about the signal flow the flywheel.

Final Brian made a not that he moved all the yanked wire. Also that there is a blue and green wires that goes to speedgoat that we as team member, we should never touch.