

# **DFIG** Design

#### University of Idaho College of Engineering

## **Project Goal**

## Project learning



IA = 2.23A P = 270W f = 60Bz NOLOAD OPEN CIRCUIT PLR = 390W 5 R = 3.9Hz LOCKED-ROTOR SHORT  $V_{DC} := \frac{7.3}{\pi} V = I_{DC} := 4.89/$  $R_1 = \frac{3}{2} \cdot \frac{V_{DC}}{I_{DC}} = 1.293 \Omega$ 

- $X_{PQ} := \frac{V_{AB}}{\sqrt{3.1_A}} = 31.068 \Omega$
- $Z_{LR} := \frac{V_{LR}}{\sqrt{3} \cdot I_{TR}} = 3.065 \,\Omega$
- $\Theta_{IR} := \operatorname{and} \left( \frac{P_{IR}}{\sqrt{3} \operatorname{Ver} k \cdot r} \right) = 49.724 det$
- $R_{LR} := Z_{LR} \cos(\Theta_R) = 1.981 \Omega$
- $R_2 := R_{1,p} R_1 = 0.63902$
- $X_{LR} := Z_{LR} \sin(Q_R) = 2.338 \Omega$
- $X_{LR, 60} = \frac{X_{LR} 60}{1.0} = 35.975 \Omega$ X1 = 0.5 XLR 60 = 17367C X2:=0.5 XLR\_60=17.987C  $X_{m} := X_{m} - X_{1} = 13.061 \Omega$



AC excitation variable frequency constant frequency operation principle of DFIG

In the first semester, our team put most efforts in learning the doubly fed induction generator and determine its parameters by doing Locked-rotor test, Noload test and DC test.



Power converter with voltage and current sensor



### Gate driver delay circuit

Driver board

We looked at several previous projects of graduate students and senior design teams from the past, and decided to design a controller system with several boards performing different tasks in order to simplify the design process and connect them using fiber-optics to reduce noise.

We reverse engineered some of the gate driver boards and drew the circuit layout, since we don't have enough time to finish the design, we are going to organize the materials we have and pass them to the next senior design teams to finish this

the microcontroller chosen in the future.

To break it down and make it simpler we started with a simple RL circuit and are now working on a existing induction machine simulink model which can be used in

# **CONCLUSION:**

Main goal of the project was to model a wind-energy learned about the machine, conducted tests to calculate the parameters of the machine used, tested existing hardware and impliented simulink model for a induction machine.



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**Model parameter**