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# DFIG AGENDA

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3/11/15

## Weekly Recap

- Presentation tomorrow 3:30pm Mccl 209
- Worked On RSCAD Model – Having Trouble duplicating constants
  - Capacitor Value
  - Current Tracking loop
    - P.300
  - Covert input currents to pu set parameters
    - Cant find his values
    - Vref?
  - Iqref to Iqref PU
    - P.277
- No Snapshot

## Need to do

- Think about what we need to revise on our snapshot poster
- Keep preparing for presentation tomorrow
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