Meeting Minutes

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April 11, 2018 1:30pm-1:45pm

Agenda Notes/Status Reports

Status: We have the whole build and capture system up and ready to go; this is our backup plan just in case we cannot get the FPGA working. With the oscilloscope setup, it only takes 20 seconds to get the data, we only need one sample which will only take 5 seconds for analysis.

The FPGA is failing the self tests, it is sending data over the Ethernet, except the data is incorrect. We plan to fully document these FPGA issues and possible solutions in the final report.

With the algorithm, we plan to be finished combining the files into one program by the end of this week or early next week. Once we are finished cleaning up the code and getting it to work, we plan to try applying a band pass filter to eliminate any excess noise in our signal.

The antenna is attached and stable, with a consistent setup.

We currently are constructing a list of tests that are being run and particular instruction distinguishing features.

Key Decisions

* Send final report to client for the month of April (instead of the monthly status report) on April 30 and then meet on May 2 for feedback.

Action Items

- * Team: Produce table of groupings of instructions of what we can and cannot distinguish from (once we get accurate hardware captures)
- * Team: Continue to build and verify FPGA design.
- * Team: Work on final report, which is due on April 30 to the client.
- * Team: Work on Acceptance Test Plan.
- * Team: Work on User Manual.