

Flash-IOT

# Weekly Team Meeting 03/12

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12 MAR 2020 / 2:30 PM / VLSI LAB

## ATTENDEES

Dr. Ay, Dr.Li, Xueren Wang, Wei Hu, Zhicai Li, Yanqin Huang, Yuhang Dai

## AGENDA

- Showing our PCB test result

## NOTES

- Showing our PCB test
  - Since our PCB board was arriving this week, we can test it based on it. The most serious problem is that when the signal received by PD is too difficult to adjust due to eight adjustable resistor. We need to change it repeated to get stable figure on oscilloscope. On the other hand, the signal after the integrated circuit has huge difference with original signal. While using subtract mode, which meaning eliminating room light noise, only in short distance like 5cm, it can work. And professor ay gives his points:
    - There may harmonic wave exist in circuit.
    - There might exist problems in our feedback loop of circuit, so, some room light noise get into feedback loop causing big difference.

## ACTION ITEMS

- Fix the circuit problem.

- Continue working communication code test with intrinsic timer.