

Meeting Minutes

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Client

April 4, 2018 1:30pm-1:45pm

Agenda Notes/Status Reports

Status: The whole memory stream is working with the ADC, now we just need to diagnose the link with the ADC. For the algorithm, we have been cleaning up code and using argparse, which improves the command line interface. We have also been writing documentation, such as the user manual and doxygen style comments within the python code.

We are building the antenna mount on the platform today for consistent results, we get the same results as the probe. After our quick status updates, we reviewed the monthly status report comments. We need to give more explanations and more details in the instruction so that future students and interns can look back on our documentation and can easily understand the documentation.

Key Decisions

* Send final report to client for the month of April (instead of the monthly status report) on April 30 and then meet on May 2 for feedback.

Action Items

* Team: Produce table of groupings of instructions of what we can and cannot distinguish from (once we get accurate hardware captures)

* Team: Build and verify FPGA design.

* Team: Work on final report, which is due on April 30 to the client.

* Team: Work on Acceptance Test Plan.

* Team: Work on user manual.