

## ## Meeting Minutes

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\*Client\*

\*March 30, 2018 1:30pm-1:45pm\*

### ### Agenda Notes/Status Reports

Status: We tracked the DMA memory issue so now we are able to transfer data. But the data from the ADC is not coming from memory correctly.

The main issue is that the KC705 is not compatible with the ADC. We plan to use our previous setup for expo with our o-scope and to give client the profiling data with the old setup within the next two weeks.

The antenna is here, and we found one that is cheaper than the Pulse Larsen (only \$35), with the same structure as our probes and it is working well (picking up signals in the same range as our probes). We plan to test it more and build a hardware mount for stability.

With the algorithm, we are waiting on new data to see if we need to change any alignment issue. Other than that, we plan to clean up code and make it more user friendly for expo.

For design expo, we plan to have students/kids remove or add boot code, run the program, and then output some sort of good or bad signal (flashing green or red lights?).

### ### Key Decisions

- \* Document FPGA/ADC problem and use o-scope if not able to diagnose issue within time constraints.

### ### Action Items

- \* Team: Detail with a block diagram the main issue with the ADC and KC705 (in the MSR).
- \* Team: Produce table of groupings of instructions of what we can and cannot distinguish from (once we get accurate hardware captures)
- \* Team: Build and verify FPGA design.
- \* Team: Monthly Status Report is due this week.
- \* Team: Work on Acceptance Test Plan.