# Data Acquisition System for Synchronous Generator Transient Performance









#### **Team: PREW Generation**

Abdulwahab Aljabrine

**Elliott Sher** 

Renjie Xia

ZhenWei Wu





#### **Problem Statement**

Design and implement a system to acquire measurements from 20 kVA synchronous generator during transient conditions





### **System Requirements**

- Must sample at 10kHz
- Measurements must be time synchronized
- Measurement must be monitored and streamed





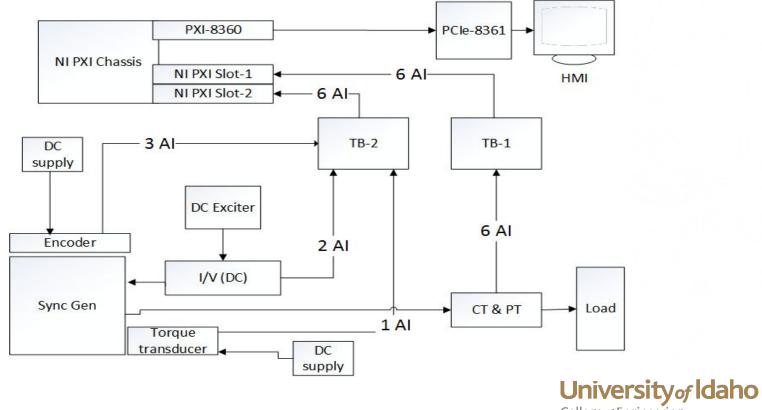
### **Measurement Requirements**

- Terminal Voltage and Current
- Field Voltage and Current
- Torque and Power
- Speed and Frequency
- Load Phase Angle





## System Topology



College of Engineering

# Equipment

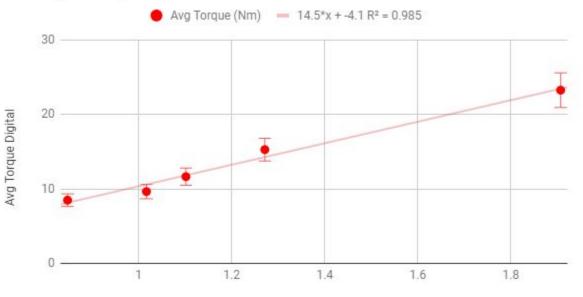
- DC Voltage Divider- Shunt resistors
- CT/PT
- Torque Transducer
- Encoder
- PXI DAQ





### **Torque Relation**

Analog vs. Digital

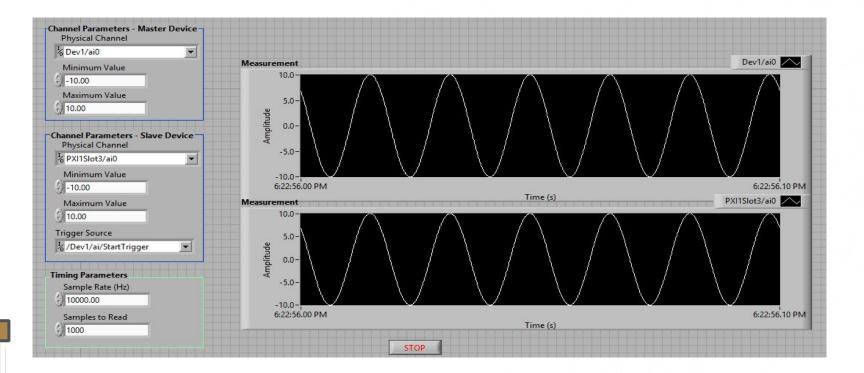


Avg Torque Estimation Analog

University of Idaho College of Engineering

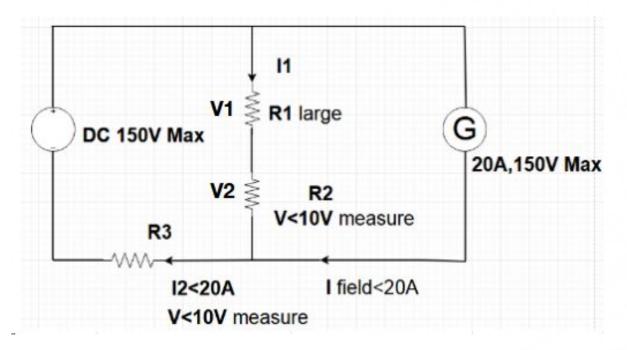


## **Synchronization**



University of Idaho College of Engineering

#### **DC** Circuit









Capstone Project Budget - Fall/Spring															
Project:SEL- Dat	a Acqu	uisiti	on Sy	stem	for Synch	hron	ous Gene	rator Tran	sient Perfo	rmance					
Last Updated	2/28/2019														
	2018												Item		
Expense Items	Se	pt	(	Oct	Nov	,	Dec	Jan	Feb	Mar	Apr		Total		
Equipment/Tools:														-	
Devices:								7							
TB 2706*2									\$ 572.4			\$	572		
Vishay Dale Resister(Res7.5)			Ì.						\$ 4.39		1	\$	4.39		
Vishay Dale Resister(Res0.1)									\$ 10.19			\$	10.19		
Ohmite Resister(Res 500)			Ĵ.						\$ 6.59			\$	6.59		
PT*5									\$ 39.8			\$	39.8		
Cable1(2 pin: DC Power)			Ĵ.						\$ 80			\$	80		
Cable2(4 pin: Analogue )									\$ 100			\$	100		
Extra Hardware	\$	-20	\$	- 20	\$ -	8			\$ -			\$	2		
Shipping fee									\$ 25			\$	25		
Poster			j.				i			\$ -		\$	Ξ.		
												\$			Expenses
											ļ į			\$ 838.41	Subtotal
											Totals	\$	838.41	\$ 4,000.00	\$ 3,161.5
														Budget	Excess



University of Idaho College of Engineering

