

---

# DFIG AGENDA

---

#11

12-9-2014

## Weekly Recap

- How did the design review go?
- How did Snapshot go?
- RSCAD model update from Normann
  - Checked model and modified a couple setting (breakers)
  - Gave suggestions on what he wanted to see
    - RLC circuit for collector bus
    - Voltages and transformers changed
- Obtained Normann's presentation and two support documents last night from Dr. Johnson

## DUE:

- **AT THE END OF THE WEEK**
  - Logbook
  - Wikipage
  - Portfolio
  - Snapshot poster